

<b>Branch: BCA</b>	<b>Semester-III</b>
<b>Subject Code: 3103</b>	<b>Lecture: 04</b> <b>Credit: 04</b>
<b>Subject Title</b>	<b>COMPUTER ORGANIZATION AND ARCHITECTURE</b>

<b>Modules</b>	<b>Sr. No.</b>	<b>Topic and Details</b>	<b>No of Lectures Assigned</b>	<b>Marks Weightage %</b>
UNIT-I	1	<b>Computer Structures:</b> Computer components, Computer functions, Basic instruction cycle (Fetch & Execute), Interrupts, Instruction Cycle with interrupts, Multiple Interrupts, I/O functions, Interconnection structure, Bus Interconnection structures, Bus structure, Multiple bus hierarchy, Elements of Bus design	8	16
UNIT-II	2	<b>Internal memory:</b> Characteristics of memory system Memory hierarchy Semiconductor main memory, Basic concepts, RAM (Static and Dynamic), ROM (PROM, EPROM, EEPROM, FLASH MEMORY), Memory Module organization, Cache Memory, Principle, Elements of cache design (Size, Mapping, Replacement, Write policies, Block size), Error detecting & correcting code	12	24
UNIT-III	3	<b>External Memory:</b> Magnetic disk, Data organization and Format, Characteristics, Disk access time, Optical Memory, CD-ROMs, WORM, Erasable optical disk, DVD, Magnetic tape, Difference between disk & tape & DVD	10	20
	4	<b>Input/Output</b> <ul style="list-style-type: none"> <li><input type="checkbox"/> Introduction</li> <li><input type="checkbox"/> Access to I/O devices</li> <li><input type="checkbox"/> I/O Modules (Functions and structures)</li> <li><input type="checkbox"/> Programmed I/O <ul style="list-style-type: none"> <li>o Overview</li> <li>o I/O Commands</li> <li>o I/O Instruction</li> <li>o Flowcharts</li> </ul> </li> <li><input type="checkbox"/> Interrupt driven I/O <ul style="list-style-type: none"> <li>o Interrupt processing</li> <li>o Design Issues</li> </ul> </li> <li><input type="checkbox"/> Drawbacks of Programmed &amp; interrupt I/O</li> <li><input type="checkbox"/> Direct Memory Access</li> </ul>	10	20

		<ul style="list-style-type: none"> <li>○ DMA Functions</li> <li>□ I/O channels &amp; Processes <ul style="list-style-type: none"> <li>Evolution of I/O Function</li> <li>Characteristics of I/O channels</li> </ul> </li> </ul>		
UNIT-IV	5	Advanced Architecture <ul style="list-style-type: none"> <li>□ Parallel Processing (SISD, SIMD, MISD and MIMD)</li> <li>□ RISC and CISC <ul style="list-style-type: none"> <li>○ Characteristics</li> <li>○ RISC Pipelining</li> </ul> </li> </ul>	10	20
		Total	50	100

**Text Books:**

1. William Stallings, "Computer Organisation and Architecture", Prentice Hall PTR, 07-Aug-2003

**References:**

1. Jain, "Modern Digital Electronic", McGraw Hill, 2008
2. Morris Mano. "Computer System Architecture", Pearson Custom Publishing, 2001
3. Hwang, "Advanced Computer Architecture", Tata McGraw Hill Education, 2003
4. Michael J. Flynn, "Computer Architecture", Narosa Publishing, 1995
5. Devale, "Computer Organisation and Architecture", 2004